abi

690034
Universal programming/test adapter

Overview

Universal programming & test package adapter for all 1.27mm pitch 0.15" wide body JEDEC SOIC devices from 8 to 16 pins.
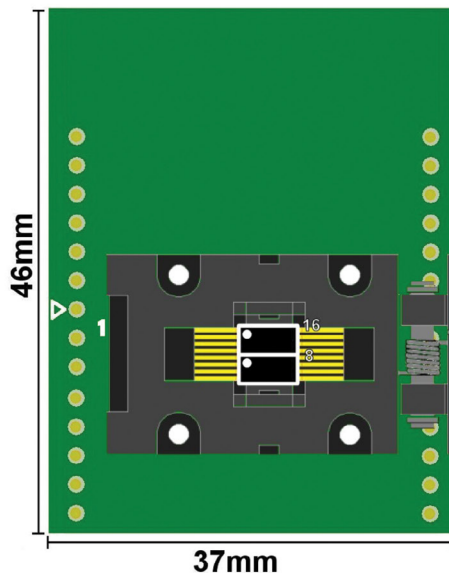
Maps to a standard 0.1" (2.54mm) pitch 0.6" wide Dual In-line Pin-out (DIP)

Description

The Clam-shell Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image

Example Devices:

1-to-1 Mapping for all 8 & 16-pin devices including Serial EEPROMs (including 24xx, 25xx & 93xx), Serial Flash and FPGA Configuration PROMs
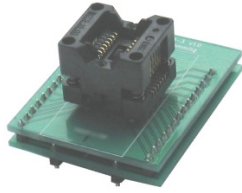
Connection Table

SOIC SOCKET	DIP BASE
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16

Notes

1. The adapter uses an IC51 series Clam-Shell Zero-Insertion Force (ZIF) socket from Yamaichi (Socket code : IC51-0162-1035)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -50 °C ~ +150 °C
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant

690035
Universal programming/test adapter

Overview
Universal programming & test package adapter for all 1.27mm pitch 0.15" wide body JEDEC SOIC devices from 8 to 16 pins.
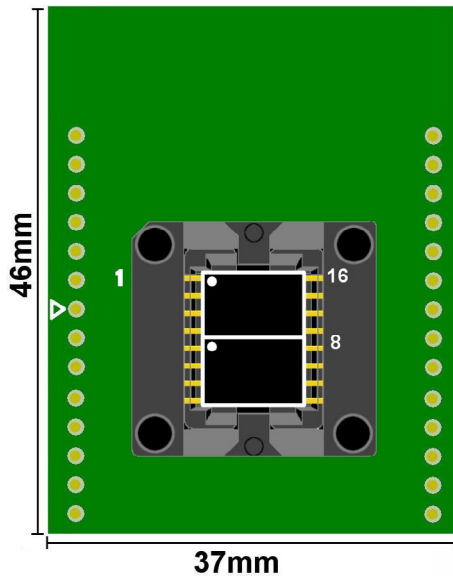
Maps to a standard 0.1" (2.54mm) pitch 0.6" wide Dual In-line Pin-out (DIP)

Description
The open-type push/pop Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image
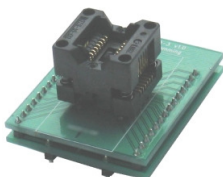
Example Devices:
1-to-1 Mapping for all 8 & 16-pin devices including Serial EEPROMs (including 24xx, 25xx & 93xx), Serial Flash and FPGA Configuration PROMs

Connection Table

SOIC SOCKET	DIP BASE
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16

Notes

1. The adapter uses an OTS series open-type push/pop Zero-Insertion Force (ZIF) socket from Enplas, with pinch contacts (Socket code : OTS-16-1.27-03)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -60 °C ~ +150 °C
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant

abi

690037

*Universal programming/test adapter*

Overview

Universal programming & test package adapter for all 1.27mm pitch 0.208" wide body **EIAJ SOIC** devices from 8 to 16 pins.

Maps to a standard 0.1" (2.54mm) pitch 0.6" wide Dual In-line Pin-out (DIP)

Description

The open-type push/pop Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

46mm

37mm

1

16

8
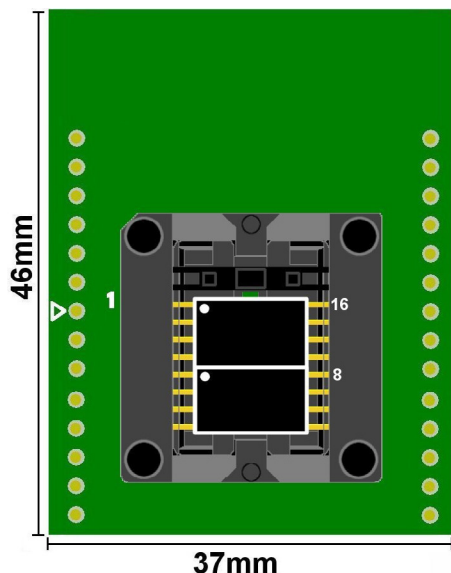
Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image

Example Devices:

1-to-1 Mapping for all 8 & 16-pin devices including Serial EEPROMs (including 24xx, 25xx & 93xx), Serial Flash, FPGA Configuration PROMs & PIC Microcontrollers

Connection Table

SOIC SOCKET	DIP BASE
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16

Notes

1. The adapter uses an OTS series open-type push/pop Zero-Insertion Force (ZIF) socket from Enplas, with pinch contacts (Socket code : OTS-16(20)-1.27-01)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -60 °C ~ +150 °C
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant
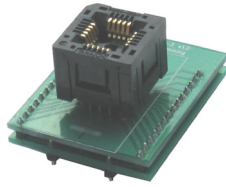
690038
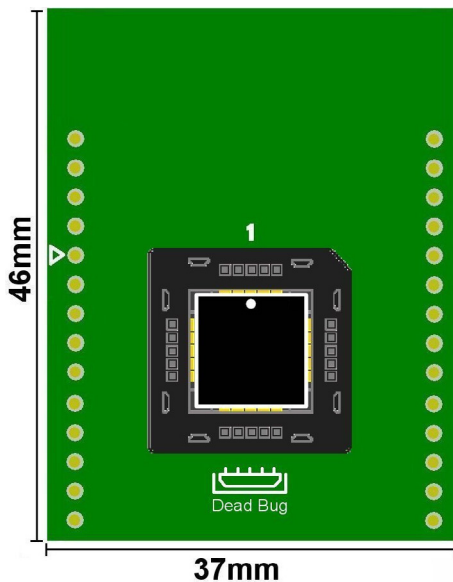*Universal programming/test adapter*

Overview
Universal programming & test package adapter for all 1.27mm pitch JEDEC standard J-lead **PLCC** devices with 20 pins.
Maps to a standard 0.1" (2.54mm) pitch 0.6" wide Dual In-line Pin-out (DIP)

Description
The open-type push/pop Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image.

Example Devices:
1-to-1 Mapping for all 20-pin devices including FPGA Configuration SPROMs (EPC, 17Cxx, etc) & Programmable logic devices (16V8, 18V8)

Connection Table

PLCC SOCKET	DIP BASE
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18
19	19
20	20

This is a Dead-bug type socket, and devices must be inserted face down.

Inserting Direction
Dead-bug type

Notes

1. The adapter uses an IC120 series open-type push/pop Zero-Insertion Force (ZIF) socket from Yamaichi, with two point contact (Socket code : IC120-0204-205)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -40 °C ~ +170 °C
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant

690039

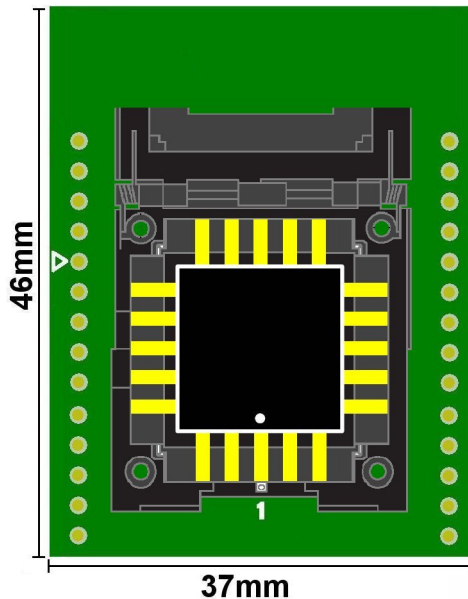*Universal programming/test adapter*

Overview

Universal clam-shell programming & test package adapter for all 1.27mm pitch JEDEC standard J-lead **PLCC** devices with 20 pins.
Maps to a standard 0.1" (2.54mm) pitch 0.6" wide Dual In-line Pin-out (DIP)

Description

The Clam-shell Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image

Example Devices:

1-to-1 Mapping for all 20-pin devices including FPGA Configuration SPROMs (EPC, 17Cxx, etc) & Programmable logic devices (16V8, 18V8)

Connection Table

PLCC SOCKET	DIP BASE
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18
19	19
20	20

Notes

1. The adapter uses an IC51 series Clam-Shell Zero-Insertion Force (ZIF) socket from Yamaichi (Socket code : IC51-0204-602)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -50 °C ~ +150 °C
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant
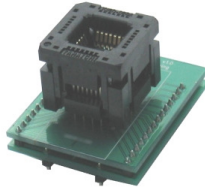
690040

Universal programming/test adapter

Overview

Programming adapter for 1.27mm pitch JEDEC standard J-lead **PLCC** programmable logic devices (PLDs) with 28 pins.
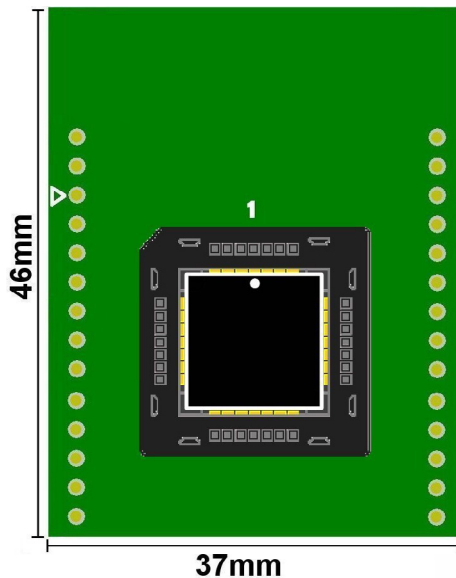
Maps to a standard 24-pin Dual In-line Pin-out (DIP) - 0.1" (2.54mm) pitch 0.6" wide

Description

The open-type push/pop Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image

Example Devices:

Maps programmable logic devices 20V8 & 22V10 to a 24-pin DIP pin-out

Connection Table

PLCC SOCKET	DIP BASE
1	Not connected
2	1
3	2
4	3
5	4
6	5
7	6
8	Not connected
9	7
10	8
11	9
12	10
13	11
14	12
15	Not connected
16	13
17	14
18	15
19	16
20	17
21	18
22	Not connected
23	19
24	20
25	21
26	22
27	23
28	24

Notes

1. The adapter uses an IC120 series open-type push/pop Zero-Insertion Force (ZIF) socket from Yamaichi, with two point contact (Socket code : IC120-0284-308)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -40 °C ~ +170 °C
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant

690041

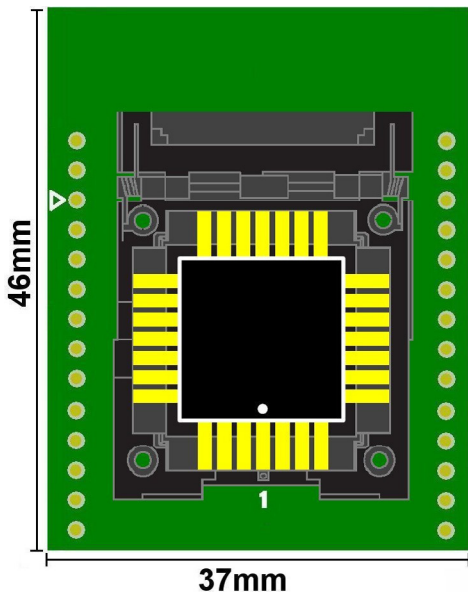*Universal programming/test adapter*

Overview

Clam-shell programming adapter for 1.27mm pitch JEDEC standard J-lead **PLCC** programmable logic devices (PLDs) with 28 pins. Maps to a standard 24-pin Dual In-line Pin-out (DIP) - 0.1" (2.54mm) pitch 0.6" wide

Description

The Clam-shell Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image
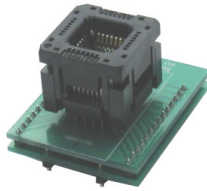
Example Devices:

Maps programmable logic devices 20V8 & 22V10 to a 24-pin DIP pin-out

Connection Table

PLCC SOCKET	DIP BASE
1	Not connected
2	1
3	2
4	3
5	4
6	5
7	6
8	Not connected
9	7
10	8
11	9
12	10
13	11
14	12
15	Not connected
16	13
17	14
18	15
19	16
20	17
21	18
22	Not connected
23	19
24	20
25	21
26	22
27	23
28	24

Notes

1. The adapter uses an IC51 series Clam-Shell Zero-Insertion Force (ZIF) socket from Yamaichi (Socket code : IC51-0284-399)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -50 ℃ ~ +150 ℃
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant

690042

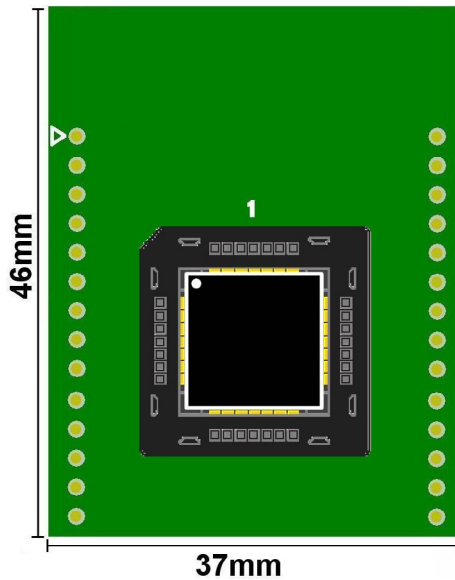*Universal programming/test adapter*

Overview

Universal programming & test package adapter for all 1.27mm pitch JEDEC standard J-lead **PLCC** devices with 28 pins.

Maps to a standard 0.1" (2.54mm) pitch 0.6" wide Dual In-line Pin-out (DIP)

Description

The open-type push/pop Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image

Example Devices:

1-to-1 Mapping for all 28-pin devices including Z8 and LPC microcontrollers, & 26V12 Programmable logic devices

Connection Table

PLCC SOCKET	DIP BASE
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18
19	19
20	20
21	21
22	22
23	23
24	24
25	25
26	26
27	27
28	28

Notes

1. The adapter uses an IC120 series open-type push/pop Zero-Insertion Force (ZIF) socket from Yamaichi, with two point contact (Socket code : IC120-0284-308)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -40 °C ~ +170 °C
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant

690043

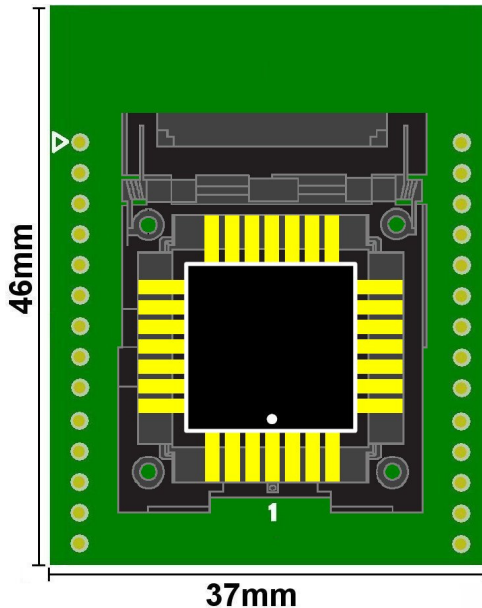*Universal programming/test adapter*

Overview

Universal Clam-shell programming & test package adapter for all 1.27mm pitch JEDEC standard J-lead **PLCC** devices with 28 pins.

Maps to a standard 0.1" (2.54mm) pitch 0.6" wide Dual In-line Pin-out (DIP)

Description

The Clam-shell Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image
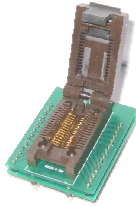
Example Devices:

1-to-1 Mapping for all 28-pin devices including Z8 and LPC microcontrollers, & 26V12 Programmable logic devices

Connection Table

PLCC SOCKET	DIP BASE
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18
19	19
20	20
21	21
22	22
23	23
24	24
25	25
26	26
27	27
28	28

Notes

1. The adapter uses an IC51 series Clam-Shell Zero-Insertion Force (ZIF) socket from Yamaichi (Socket code : IC51-0284-399)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -50 °C ~ +150 °C
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant

690044
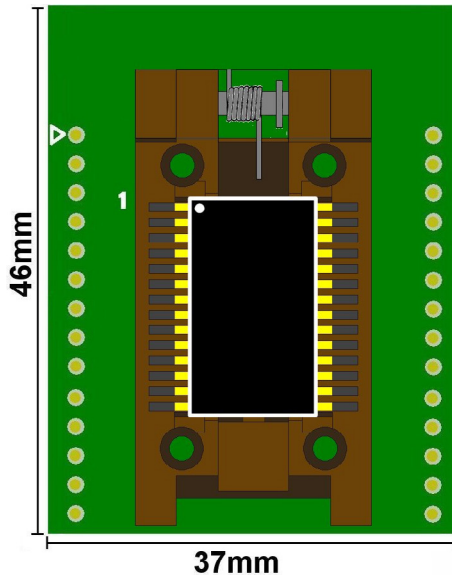*Universal programming/test adapter*

Overview

Universal Clam-shell programming & test package adapter for all 1.27mm pitch 0.3" wide body JEDEC SOIC devices from 18 to 28 pins. Maps to a standard 0.1" (2.54mm) pitch 0.6" wide Dual In-line Pin-out (DIP)

Description

The Clam-Shell Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

46mm

37mm

Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image

Example Devices:

1-to-1 Mapping for all 18 to 28-pin devices including Parallel EEPROMs, Programmable Logic (PLD) devices, 89Cx051, Microchip PICs, Motorola HC05/08 & Philips LPC microcontrollers
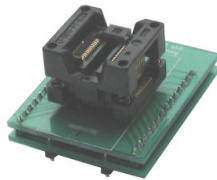
Connection Table

SOIC SOCKET	DIP BASE
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18
19	19
20	20
21	21
22	22
23	23
24	24
25	25
26	26
27	27
28	28

Notes

1. The adapter uses an FP series Clam-Shell Zero-Insertion Force (ZIF) socket from Enplas. (Socket code : FP-28-1.27-08)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -40 °C ~ +150 °C
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant

690045

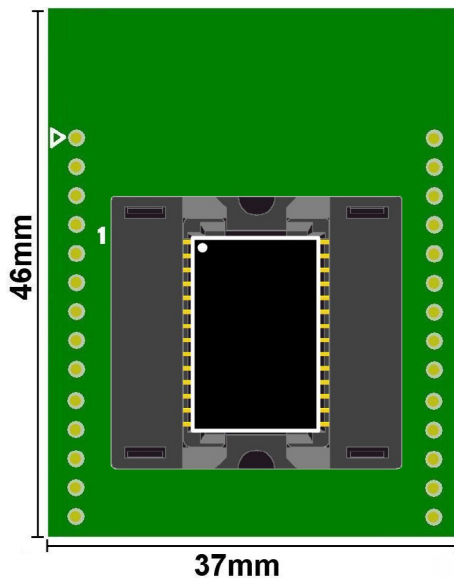*Universal programming/test adapter*

Overview

Universal programming & test package adapter for all 1.27mm pitch 0.3" wide body JEDEC SOIC devices from 18 to 28 pins.

Maps to a standard 0.1" (2.54mm) pitch 0.6" wide Dual In-line Pin-out (DIP)

Description

The open-type push/pop Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image

Example Devices:

1-to-1 Mapping for all 18 to 28-pin devices including Parallel EEPROMs, Programmable Logic (PLD) devices, 89Cx051, Microchip PICs, Motorola HC05/08 & Philips LPC microcontrollers

Connection Table

SOIC SOCKET	DIP BASE
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18
19	19
20	20
21	21
22	22
23	23
24	24
25	25
26	26
27	27
28	28

Notes

1. The adapter uses a 652 series open-type push/pop Zero-Insertion Force (ZIF) socket from Wells, with pin contacts (Socket code : 652D0282211)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -40 °C ~ +150 °C
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant
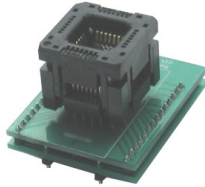
690046

Universal programming/test adapter

Overview

Universal programming & test package adapter for all 32-pin 1.27mm pitch J-lead PLCC devices..
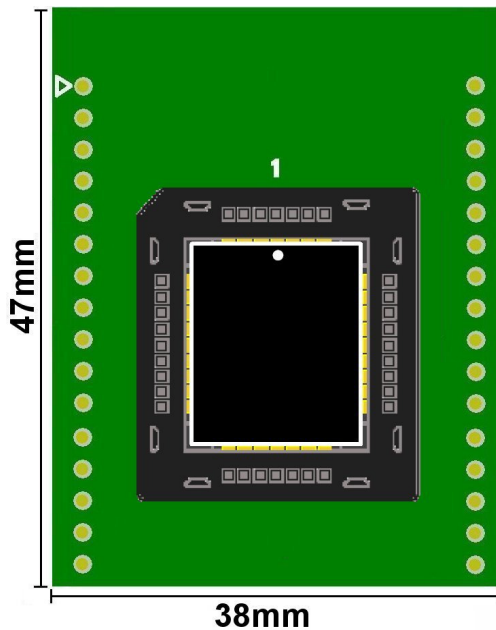
Maps to a standard 0.1" (2.54mm) pitch 0.6" wide Dual In-line Pin-out (DIP)

Description

The open-type push/pop Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image

Example Devices:

1-to-1 Mapping for all 32-pin PLCC devices including 1Mbit+ EPROMs, EEPROMs & Flash memory

Connection Table

PLCC SOCKET	DIP BASE
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18
19	19
20	20
21	21
22	22
23	23
24	24
25	25
26	26
27	27
28	28
29	29
30	30
31	31
32	32

Notes

1. The adapter uses an IC120 series open-type push/pop Zero-Insertion Force (ZIF) socket from Yamaichi, with two point contacts (Socket code : IC120-0324-309)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -40 °C ~ +150 °C
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant

690047

Universal programming/test adapter

Overview
Universal programming & test package adapter for all 32-pin 1.27mm pitch J-lead PLCC devices..
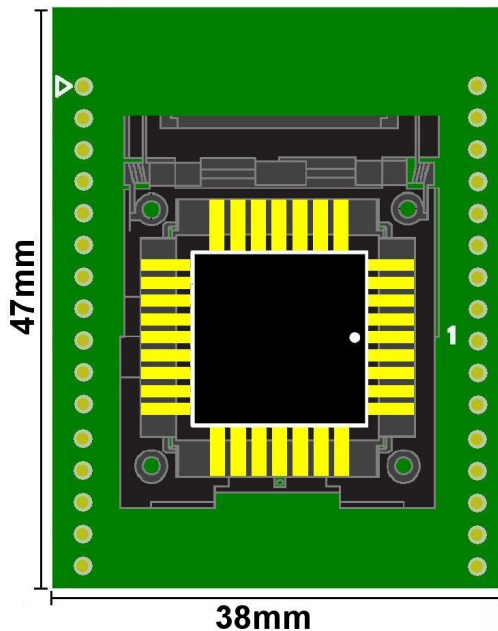Maps to a standard 0.1" (2.54mm) pitch 0.6" wide Dual In-line Pin-out (DIP)

Description
The Clam-shell Zero-Insertion Force (ZIF) socket allows easy insertion and removal of devices.

The adapter is for use on any programming or test equipment.

Device Alignment

47mm

38mm

Pin 1 of the DIP base, and Pin 1 of the Device are both marked on the PCB, as per the above image

Example Devices:
1-to-1 Mapping for all 32-pin PLCC devices including 1Mbit+ EPROMs, EEPROMs & Flash memory

Connection Table

PLCC SOCKET	DIP BASE
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18
19	19
20	20
21	21
22	22
23	23
24	24
25	25
26	26
27	27
28	28
29	29
30	30
31	31
32	32

Notes

1. The adapter uses an IC51 series Clam-Shell Zero-Insertion Force (ZIF) socket from Yamaichi, with (Socket code : IC120-0324-309)
2. It has a lifecycle of 10,000 insertion cycles (minimum) under normal use
3. Operating temperature range : -50 °C ~ +150 °C
4. Printed circuit board material : FR4 with 1 once copper with a Nickel/Gold RoHS compliant finish
5. Adapter is lead-free and RoHS compliant